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FIG.1A

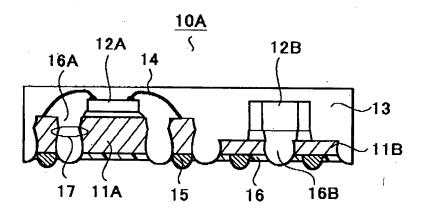


FIG.1B.

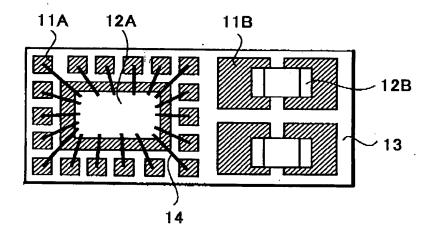
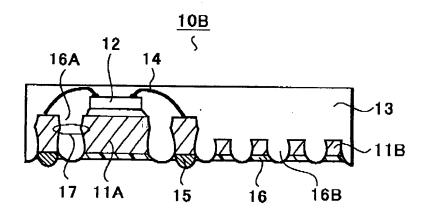


FIG.2



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FIG.3

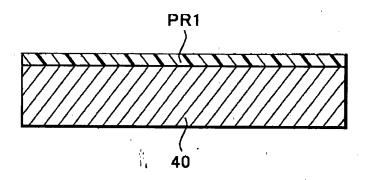


FIG.4

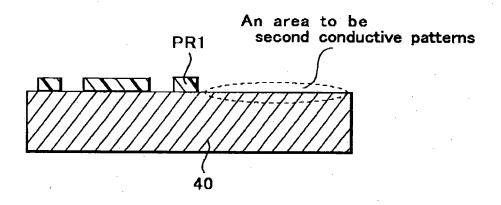
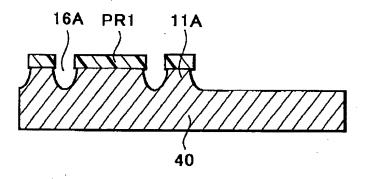


FIG.5



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FIG.6

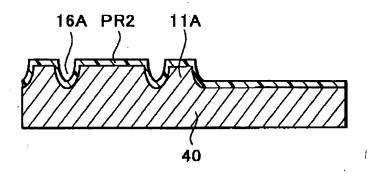


FIG.7A

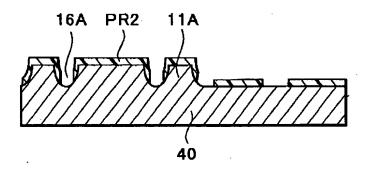
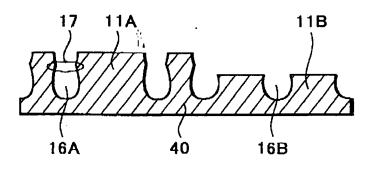


FIG.7B



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FIG.8A

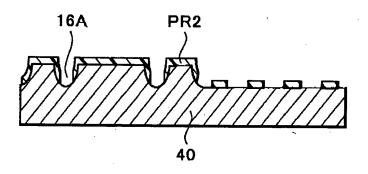
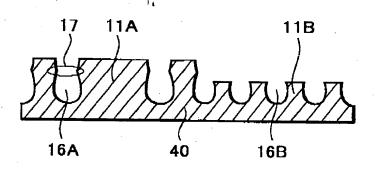


FIG.8B



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FIG.9A

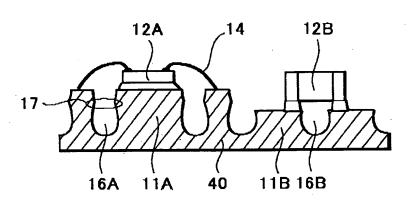
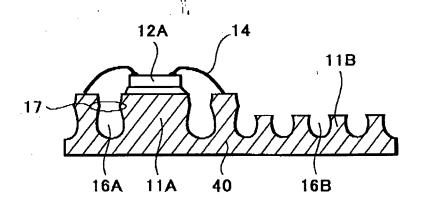


FIG.9B



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FIG.10A

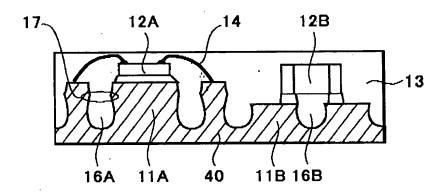
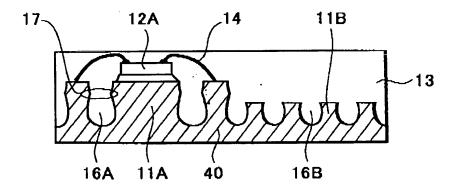


FIG.10B



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FIG.11A

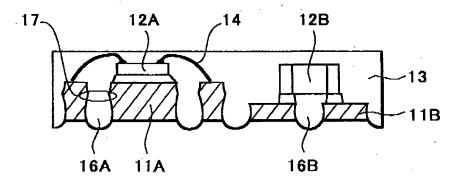
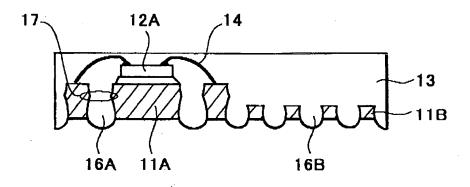


FIG.11B



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FIG.12A

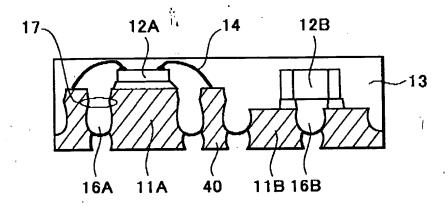


FIG.12B

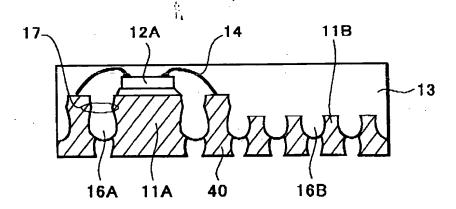


FIG.13

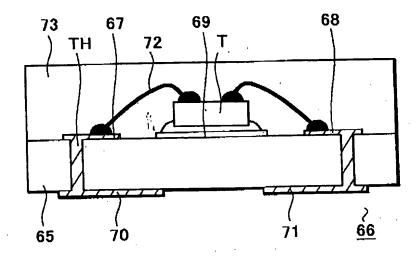


FIG.14

